

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Taro FUJII et al.

Conf. 6783

Application No. 10/694,822

Group 2183

Filed October 29, 2003

Examiner D. Cody

ARRAY-TYPE PROCESSOR HAVING PLURAL
PROCESSOR ELEMENTS CONTROLLED BY A STATE
CONTROL UNIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 10, 2007

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying document, a copy of which is attached to this statement, is made of record on the enclosed Form PTO-1449.

A concise explanation of the relevance of this item is that this reference was cited by the Japanese Patent Office in an Official Action. A copy of the Japanese Official Action in which it was cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. **An English translation of the enclosed portion is also attached hereto.** JP 2001-312481 was previously cited in the Information Disclosure Statement filed October 29, 2003.

Respectfully submitted,

YOUNG & THOMPSON



Robert J. Patch, Reg. No. 17,355
745 South 23rd Street
Arlington, VA 22202
Telephone (703) 521-2297
Telefax (703) 685-0573

RJP/lk



Application No.:
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(Use several sheets if necessary)

Applicant:
Taro FUJII et al.

Filing Date:
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Group Art Unit:
2183

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER:

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

* Abstract provided for the Examiner's convenience